



#10 / Notice of Appeal  
PATENT  
1/7/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kiran Ganesh et al. Examiner: Phallaka Kik  
Serial No.: 09/431,477 Group Art Unit: 2825  
Filed: November 1, 1999 Docket: 884.141US1  
Title: 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR  
VLSI DESIGN

**NOTICE OF APPEAL FROM THE DECISION OF THE EXAMINER**  
**TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

**BOX AF**

Commissioner for Patents  
Washington, D.C. 20231

In compliance with 37 C.F.R. § 1.191, Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated June 26, 2002, of the Examiner rejecting claims 1, 2, 4-8, 10-23, 25-27, and 29-31 of the above-identified patent application.

A request for an extension of time to respond to the Examiner's rejection is submitted herewith along with payment of the required extension fee.

Please charge the amount of \$320.00 in payment of the Notice of Appeal fee under 37 C.F.R. § 1.17(b), as well as any additional required fees, to Deposit Account No. 19-0743.

Respectfully submitted,

KIRAN GANESH ET AL.

By Applicants' Attorneys,

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Date December 26, 2002 By Ann M. McCrackin  
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Reg. No. 42,858

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to BOX AF, Commissioner of Patents, Washington, D.C. 20231 on December 26, 2002.

Name

Tina Kohout

Signature

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